



MSDWI03 Series
Electric Characteristic Note

MSDWI03 Series EC Note

DC-DC CONVERTER 3W, SMD Package

Features

- ▶ Compact SMD Package
- ▶ Ultra-wide 4:1 Input Voltage Range
- ▶ Fully Regulated Output Voltage
- ▶ I/O Isolation 1500 VDC
- ▶ Operating Ambient Temp. Range -40°C to +80°C
- ▶ Under-voltage, Overload and Short Circuit Protection
- ▶ Remote On/Off Control
- ▶ Cleaning-washable Process Available(option)
- ▶ Qualified for Lead-free Reflow Solder Process
According to IPC/JEDECJ-STD-020D.1
- ▶ Tape & Reel Package Available
- ▶ UL/cUL/IEC/EN 62368-1(60950-1) Safety Approval



Applications

- ▶ Distributed power architectures
- ▶ Workstations
- ▶ Computer equipment
- ▶ Communications equipment

Product Overview

Minmax's MSDWI03 series power modules are in mini-SMD DC-DC converters that operate over input voltage ranges of 9-36VDC and 18-75VDC which provide precisely regulated output voltages of 3.3V, 5V, 12V, 15V, 24V, ±5V, ±12V and ±15VDC. Pin compatible with the MDW1000 series, the MDWI03 offers a power rating up to 3W and a typical full-load efficiency of 80%, under-voltage, over load/short circuit protection and remote on/off control. The MSDWI03 series is an excellent selection for data communication equipment, mobile battery driven equipment, distributed power system, telecommunication equipment, mixed analog/digital subsystem, process/machine control equipment, computer peripheral equipment and industrial robot system.

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Model Number	Input Voltage (Range)	Output Voltage	Output Current		Input Current		Max. capacitive Load	Efficiency (typ.) @Max. Load	
			Max.	Min.	@Max. Load	@No Load			
			VDC	VDC	mA	mA	mA(typ.)	mA(typ.)	μF
MSDWI03-24S033	24 (9 ~ 36)	3.3	600	90	110		30	220	75
MSDWI03-24S05		5	600	90	160			220	78
MSDWI03-24S12		12	250	38	156			47	80
MSDWI03-24S15		15	200	30	156			47	80
MSDWI03-24S24		24	125	19	156			47	80
MSDWI03-24D05		±5	±300	±45	162			47#	77
MSDWI03-24D12		±12	±125	±19	156			47#	80
MSDWI03-24D15		±15	±100	±15	156			47#	80
MSDWI03-48S033	48 (18 ~ 75)	3.3	600	90	55		20	220	75
MSDWI03-48S05		5	600	90	80			220	78
MSDWI03-48S12		12	250	38	78			47	80
MSDWI03-48S15		15	200	30	78			47	80
MSDWI03-48S24		24	125	19	78			47	80
MSDWI03-48D05		±5	±300	±45	81			47#	77
MSDWI03-48D12		±12	±125	±19	78			47#	80
MSDWI03-48D15		±15	±100	±15	78			47#	80

For each output

Input Specifications						
Parameter	Model	Min.	Typ.	Max.	Unit	
Input Surge Voltage (1 sec. max.)	24V Input Models	-0.7	---	50	VDC	
	48V Input Models	-0.7	---	100		
Start-Up Threshold Voltage	24V Input Models	4.5	6	8.5		
	48V Input Models	8.5	12	17		
Under Voltage Shutdown	24V Input Models	---	---	8		
	48V Input Models	---	---	16		
Short Circuit Input Power		---	---	2000	mW	
Input Filter	All Models	Internal Pi Type				
Conducted EMI		Compliance to EN 55022, class A				

Remote On/Off Control						
Parameter	Conditions	Min.	Typ.	Max.	Unit	
Converter On	2.5V ~ 5.5V or Open Circuit					
Converter Off	-0.7V ~ 0.8V					
Control Input Current (on)	Vctrl = Min. to Max.	---	---	-400	μA	
Control Input Current (off)	Vctrl = Min. to Max.	---	---	-400	μA	
Control Common	Referenced to Negative Input					
Standby Input Current	Nominal Vin	---	---	5	mA	

Output Specifications						
Parameter	Conditions	Min.	Typ.	Max.	Unit	
Output Voltage Setting Accuracy		---	---	±2.0	%Vnom.	
Output Voltage Balance	Dual Output, Balanced Loads	---	±1.0	±2.0	%	
Line Regulation	Vin=Min. to Max. @Full Load	---	±0.5	±1.0	%	
Load Regulation	I _o =15% to 100%	---	±0.5	±1.2	%	
Ripple & Noise	0-20 MHz Bandwidth	---	---	100	mV _{P-P}	
Transient Recovery Time	25% Load Step Change	---	300	600	μsec	
Transient Response Deviation		---	±3	---	%	
Temperature Coefficient		---	±0.01	±0.02	%/°C	
Over Load Protection	Foldback	110	150	---	%	
Short Circuit Protection		Continuous, Automatic Recovery				

General Specifications

Parameter	Conditions	Min.	Typ.	Max.	Unit
I/O Isolation Voltage	60 Seconds	1500	---	---	VDC
	1 Second	1800	---	---	VDC
I/O Isolation Resistance	500 VDC	1000	---	---	MΩ
I/O Isolation Capacitance	100kHz, 1V	---	350	500	pF
Switching Frequency		---	350	---	kHz
MTBF (calculated)	MIL-HDBK-217F@25°C, Ground Benign	300,000			Hours
Moisture Sensitivity Level (MSL)	IPC/JEDEC J-STD-020D.1	Level 2			
Safety Approvals	UL/cUL 62368-1 recognition(UL certificate), IEC/EN 62368-1 & 60950-1(CB-report)				

Environmental Specifications

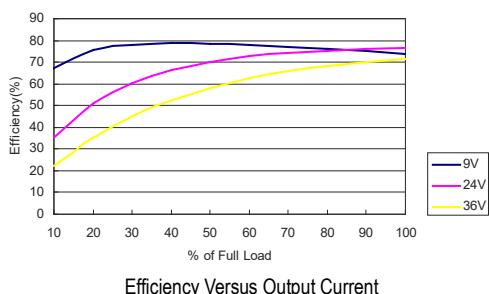
Parameter	Min.	Max.	Unit
Operating Ambient Temperature Range (See Power Derating Curve)	-40	+80	°C
Case Temperature	---	+105	°C
Storage Temperature Range	-50	+125	°C
Humidity (non condensing)	---	95	% rel. H
Lead-free Reflow Solder Process	IPC/JEDEC J-STD-020D.1		

Notes

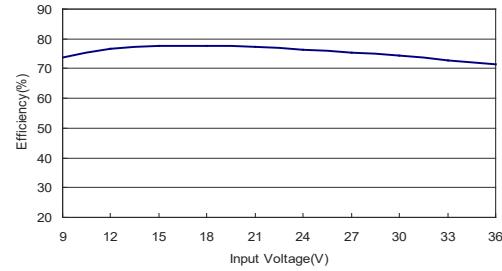
- 1 Specifications typical at Ta=+25°C, resistive load, nominal input voltage and rated output current unless otherwise noted.
- 2 Transient recovery time is measured to within 1% error band for a step change in output load of 75% to 100%.
- 3 These power converters require a minimum output loading to maintain specified regulation, operation under no-load conditions will not damage these modules; however, they may not meet all specifications listed.
- 4 We recommend to protect the converter by a slow blow fuse in the input supply line.
- 5 Other input and output voltage may be available, please contact MINMAX.
- 6 Specifications are subject to change without notice.
- 7 The repeated high voltage isolation testing of the converter can degrade isolation capability, to a lesser or greater degree depending on materials, construction, environment and reflow solder process. Any material is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage. Furthermore, the high voltage isolation capability after reflow solder process should be evaluated as it is applied on system.

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-24S033

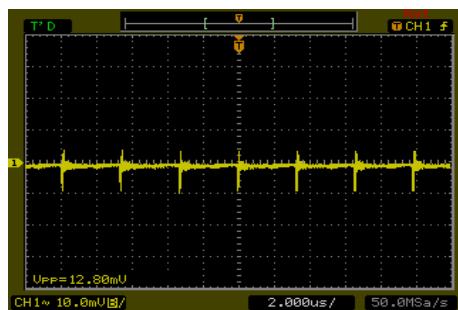


Efficiency Versus Output Current



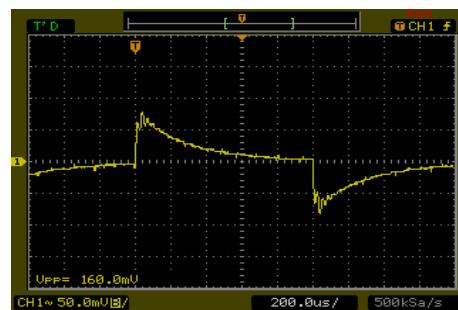
Efficiency Versus Input Voltage

Full Load



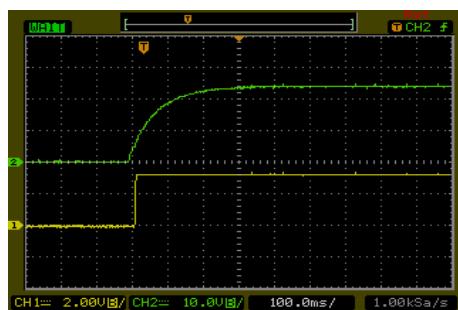
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



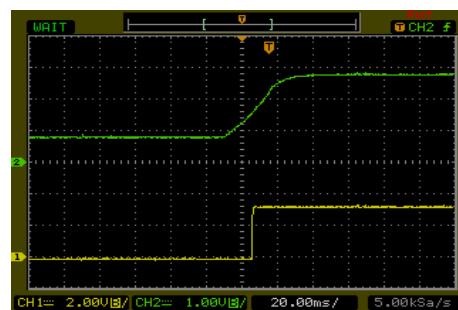
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



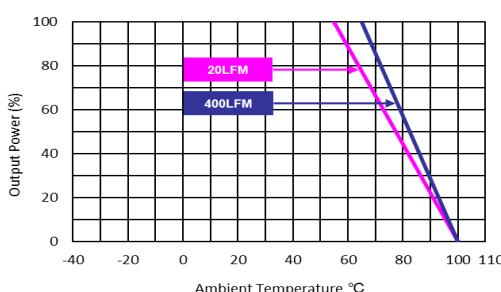
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

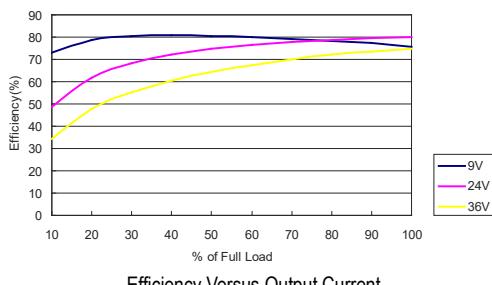


Derating Output Current Versus Ambient Temperature and Airflow

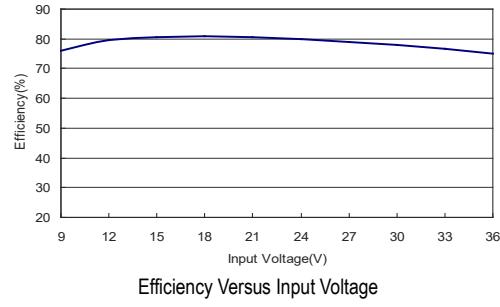
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-24S05

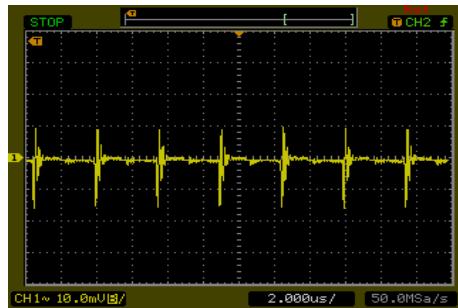


Efficiency Versus Output Current



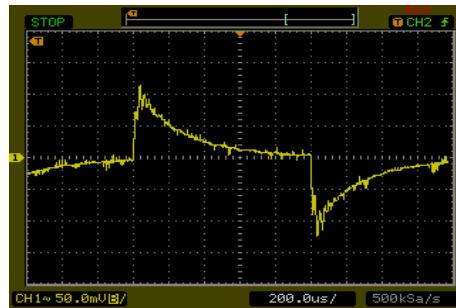
Efficiency Versus Input Voltage

Full Load



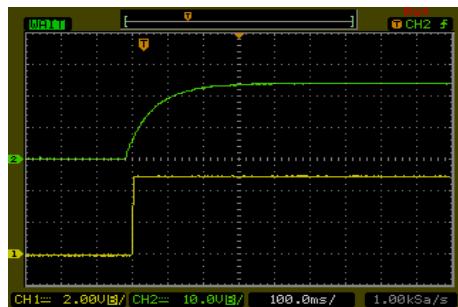
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



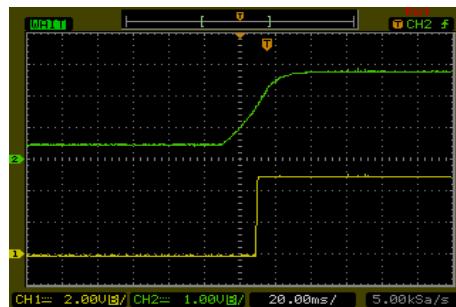
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



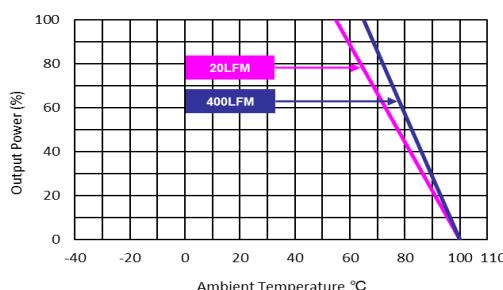
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

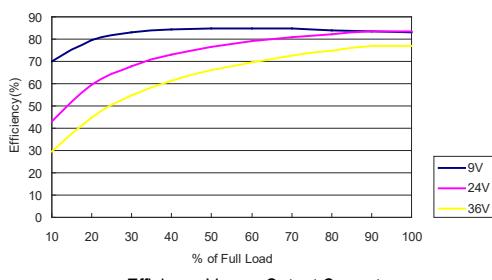


Derating Output Current Versus Ambient Temperature and Airflow

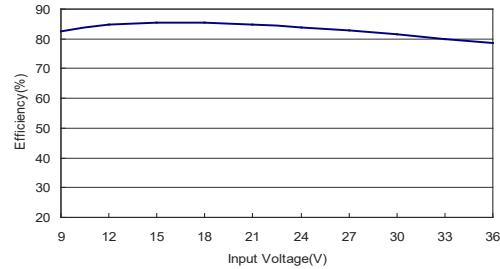
$V_{in}=V_{in\ nom}$

Characteristic Curves

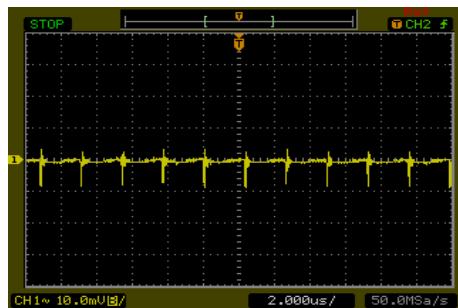
All test conditions are at 25°C. The figures are identical for MSDWI03-24S12



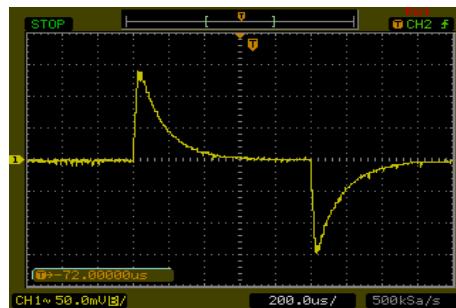
Efficiency Versus Output Current



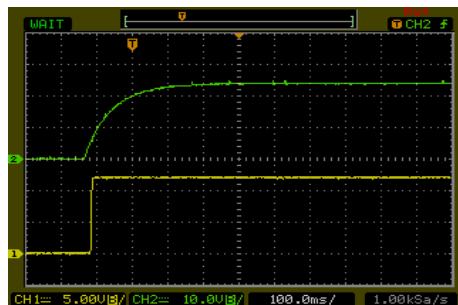
Efficiency Versus Input Voltage
Full Load



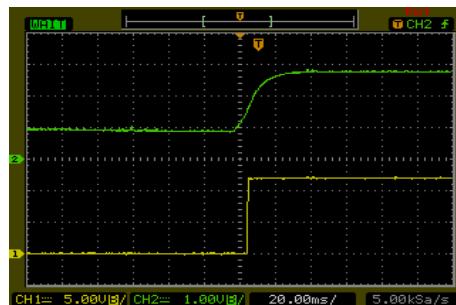
Typical Output Ripple and Noise
 $V_{in}=V_{in\ nom}$; Full Load



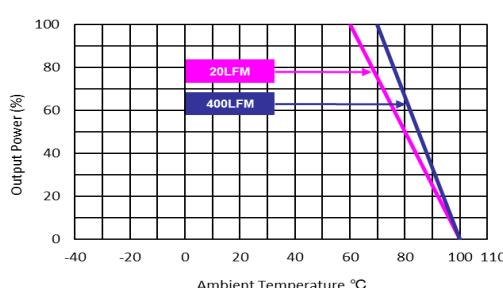
Transient Response to Dynamic Load Change
from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



Typical Input Start-Up and Output Rise Characteristic
 $V_{in}=V_{in\ nom}$; Full Load



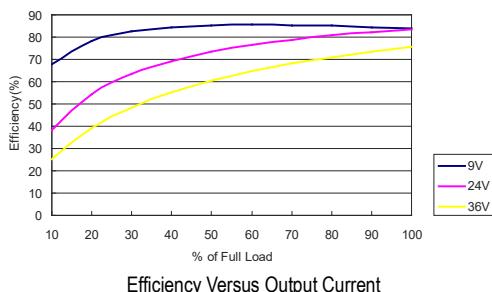
ON/OFF Voltage Start-Up and Output Rise Characteristic
 $V_{in}=V_{in\ nom}$; Full Load



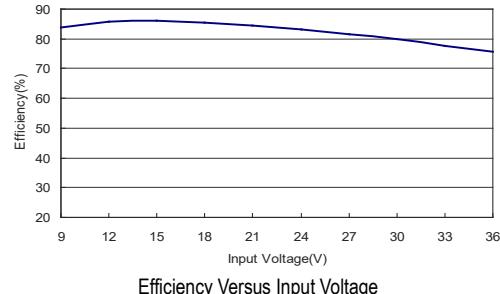
Derating Output Current Versus Ambient Temperature and Airflow
 $V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-24S15

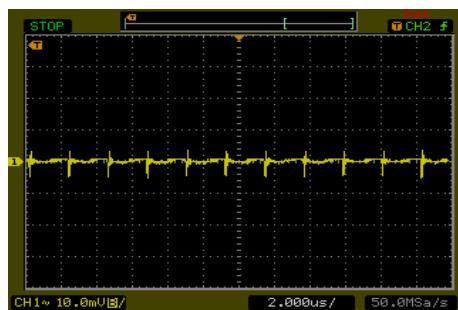


Efficiency Versus Output Current



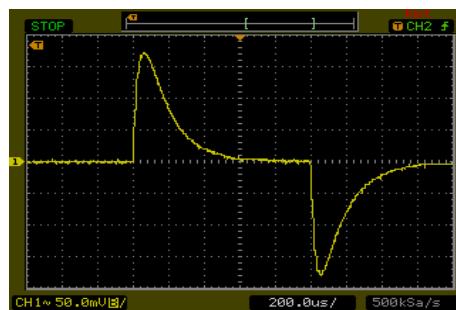
Efficiency Versus Input Voltage

Full Load



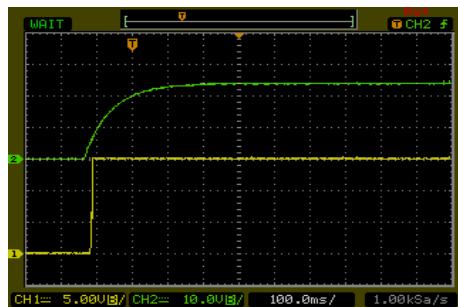
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



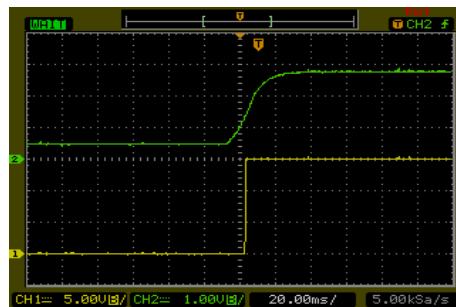
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



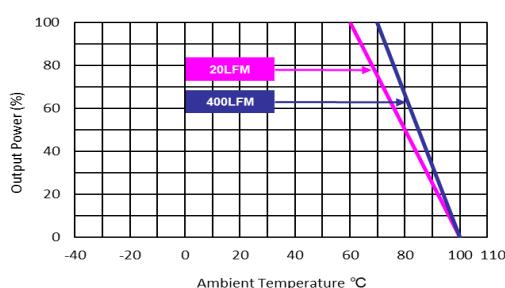
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

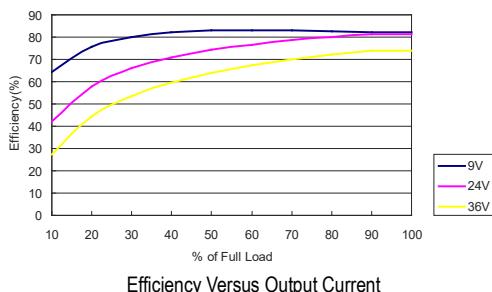


Derating Output Current Versus Ambient Temperature and Airflow

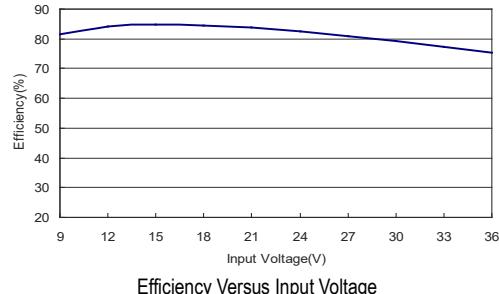
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-24S24

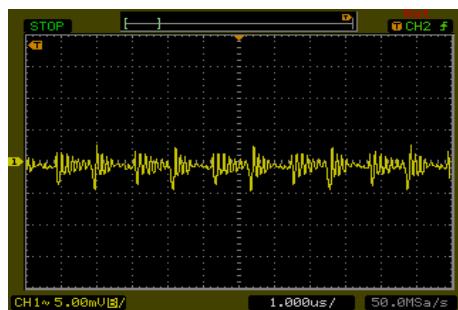


Efficiency Versus Output Current



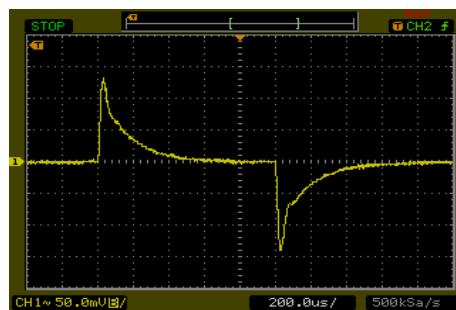
Efficiency Versus Input Voltage

Full Load



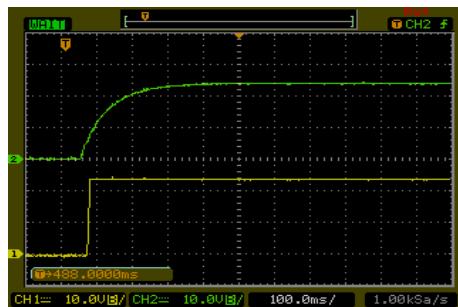
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



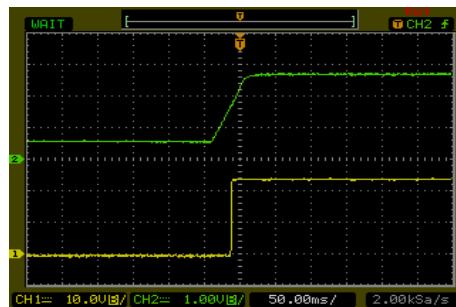
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



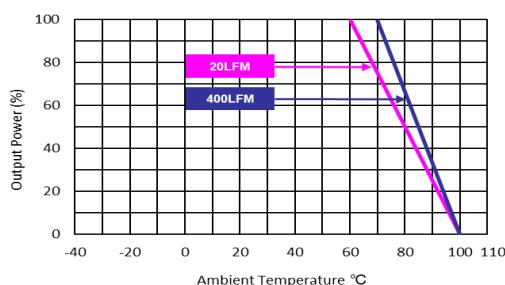
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

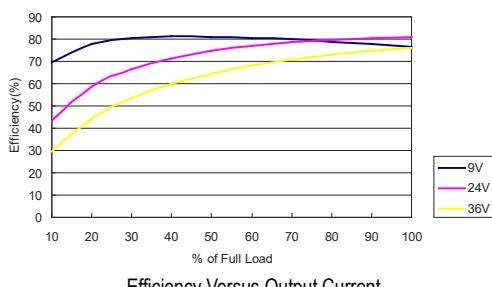


Derating Output Current Versus Ambient Temperature and Airflow

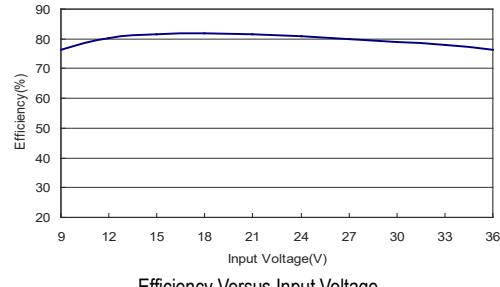
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-24D05

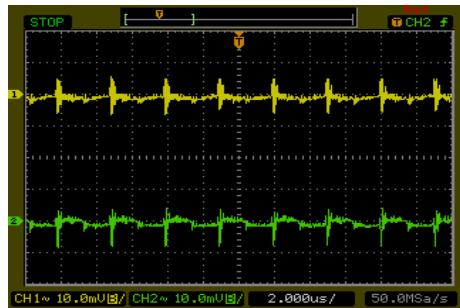


Efficiency Versus Output Current



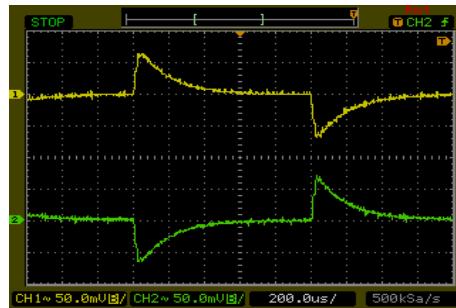
Efficiency Versus Input Voltage

Full Load



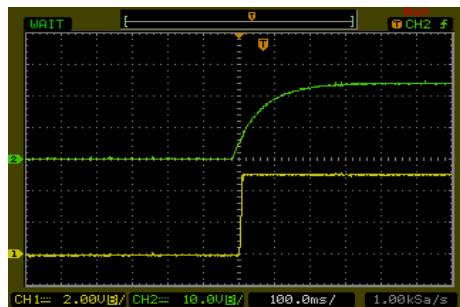
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



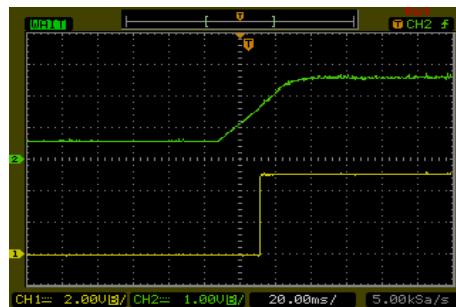
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



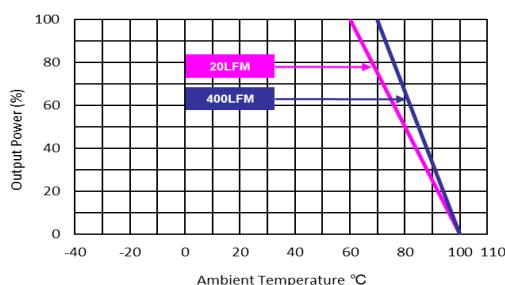
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

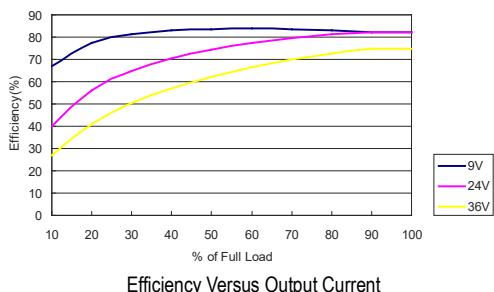


Derating Output Current Versus Ambient Temperature and Airflow

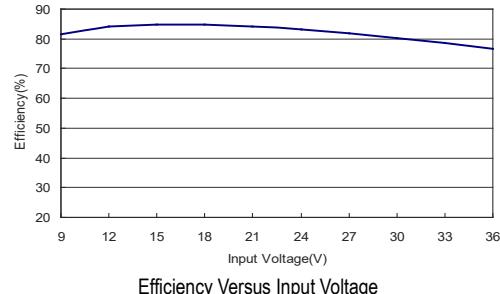
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-24D12

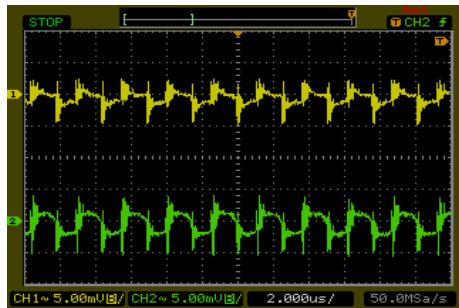


Efficiency Versus Output Current



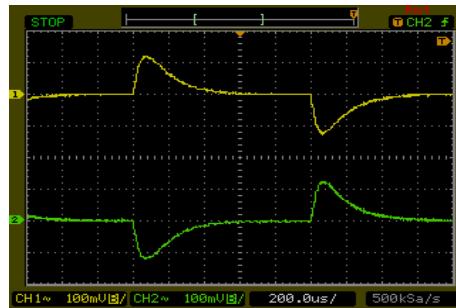
Efficiency Versus Input Voltage

Full Load



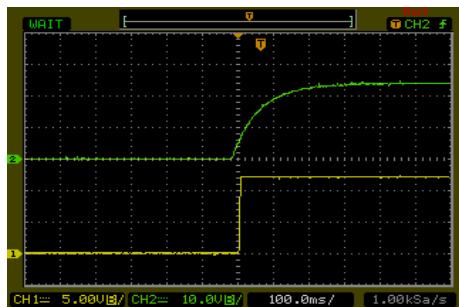
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



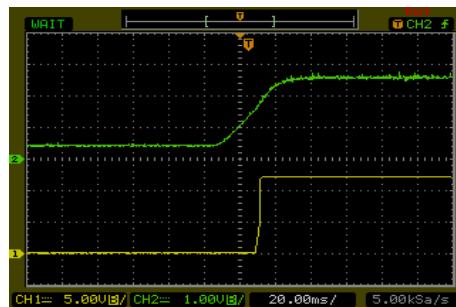
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



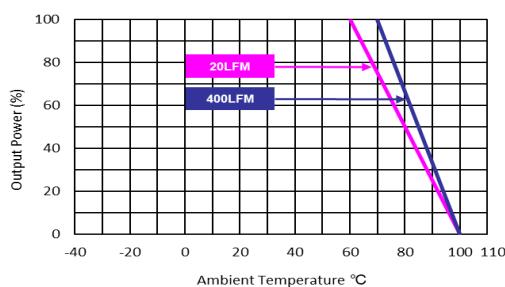
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

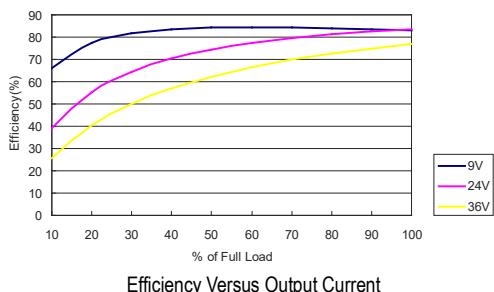


Derating Output Current Versus Ambient Temperature and Airflow

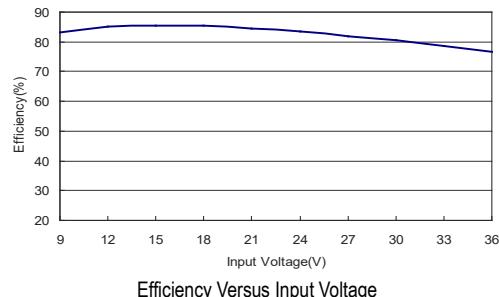
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-24D15



Efficiency Versus Output Current



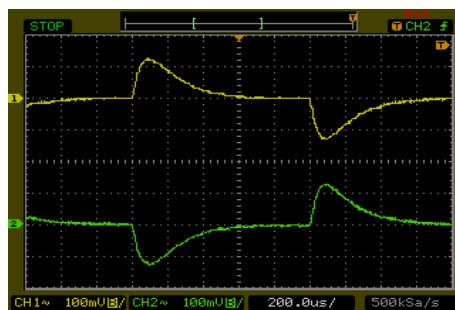
Efficiency Versus Input Voltage

Full Load



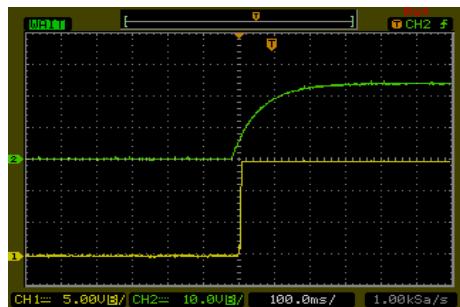
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



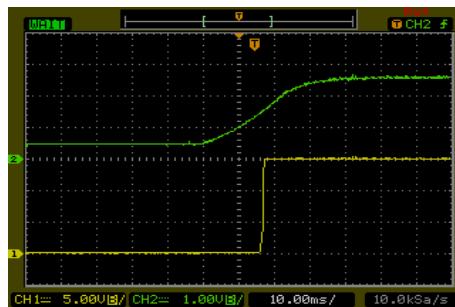
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



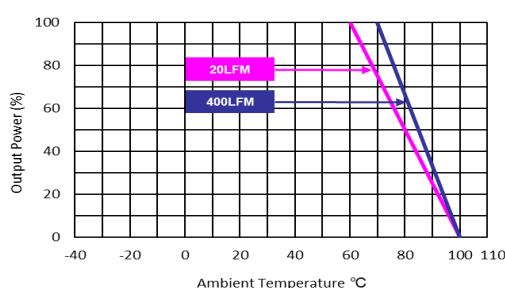
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

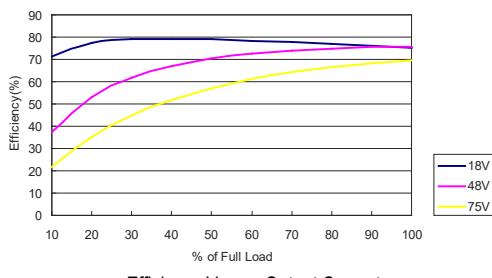


Derating Output Current Versus Ambient Temperature and Airflow

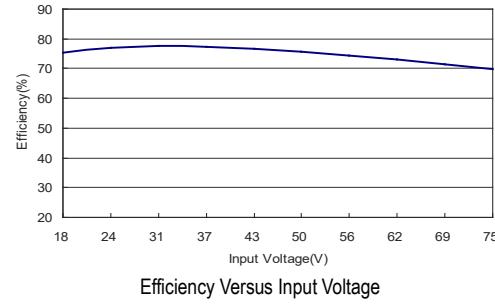
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-48S033

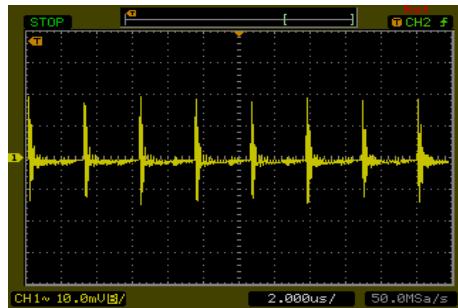


Efficiency Versus Output Current



Efficiency Versus Input Voltage

Full Load



Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



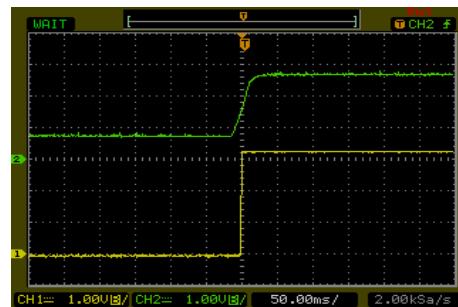
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



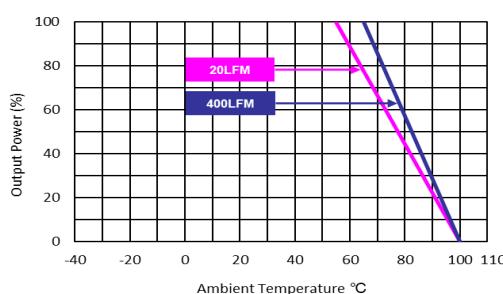
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

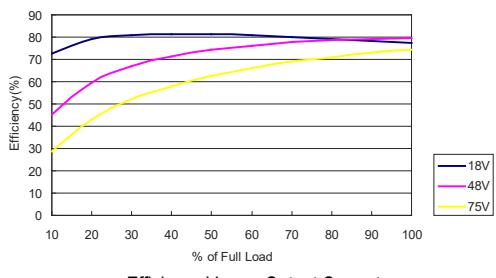


Derating Output Current Versus Ambient Temperature and Airflow

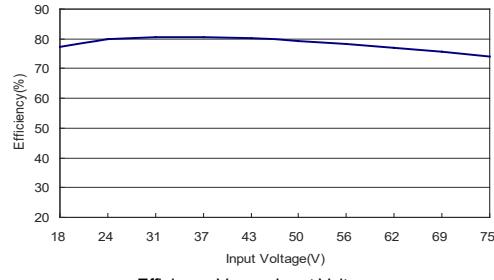
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-48S05

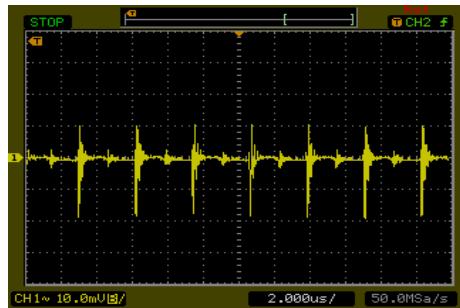


Efficiency Versus Output Current



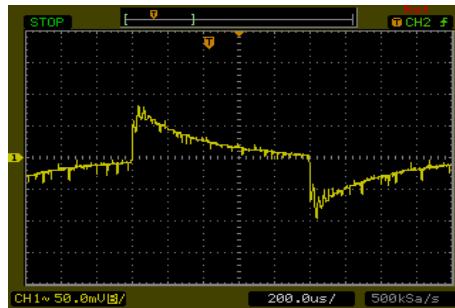
Efficiency Versus Input Voltage

Full Load



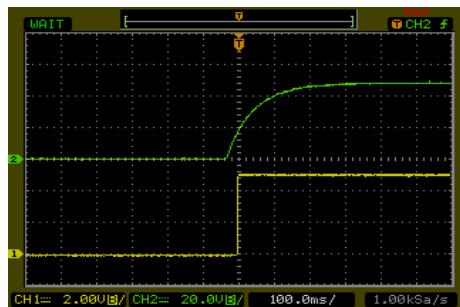
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



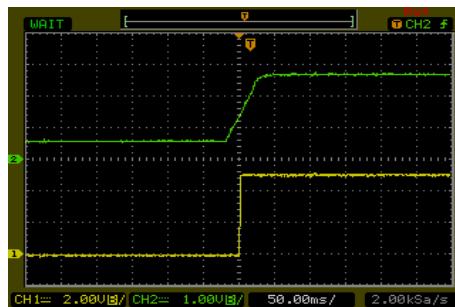
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



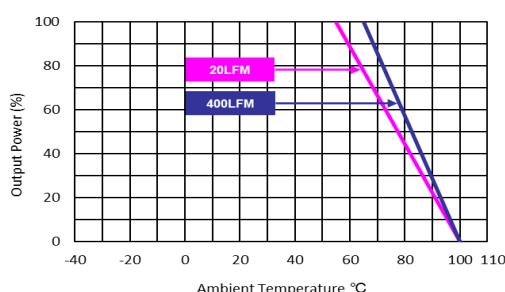
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

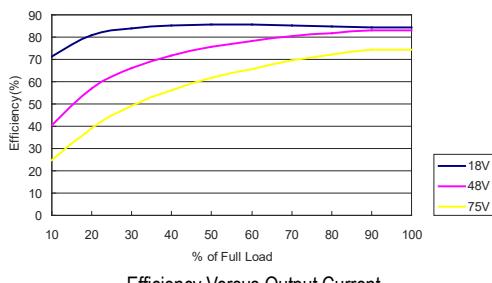


Derating Output Current Versus Ambient Temperature and Airflow

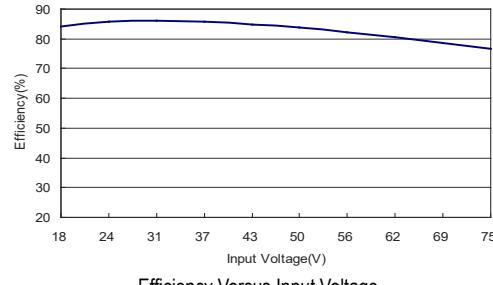
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-48S12

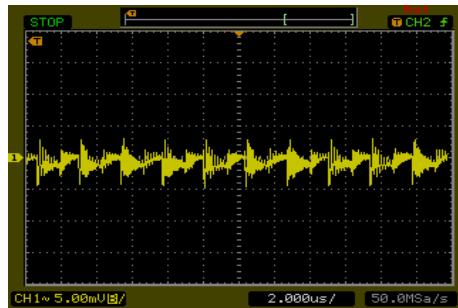


Efficiency Versus Output Current



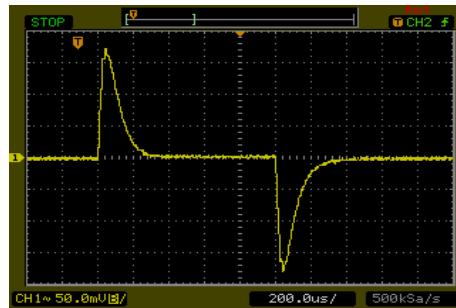
Efficiency Versus Input Voltage

Full Load



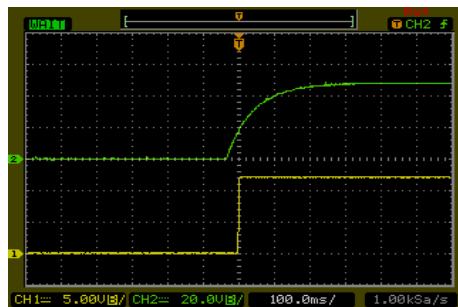
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



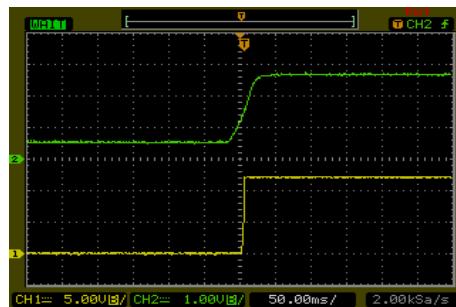
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



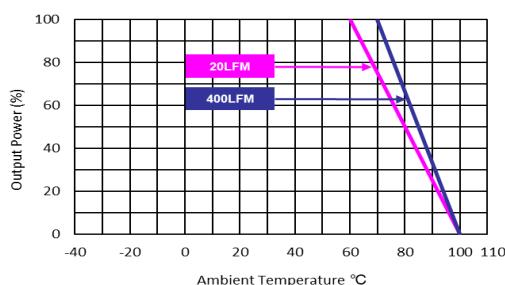
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

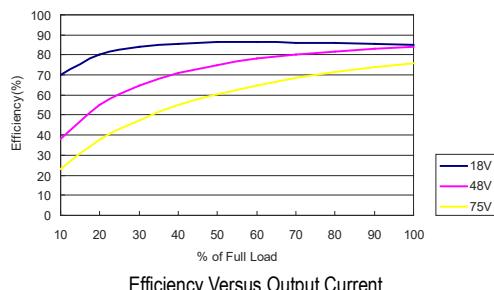


Derating Output Current Versus Ambient Temperature and Airflow

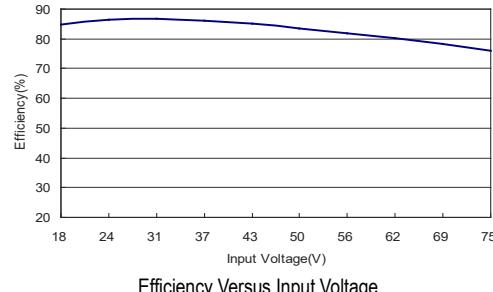
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-48S15

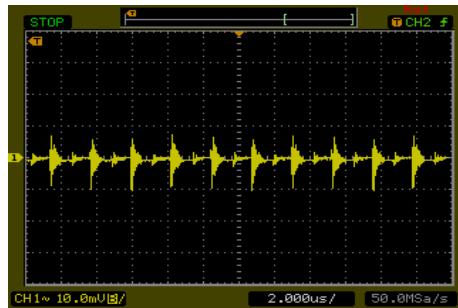


Efficiency Versus Output Current



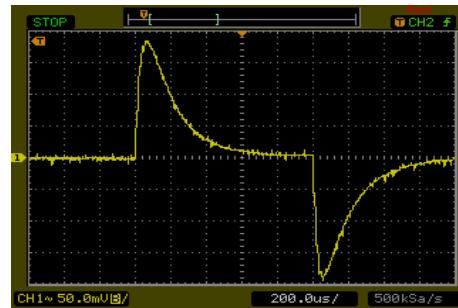
Efficiency Versus Input Voltage

Full Load



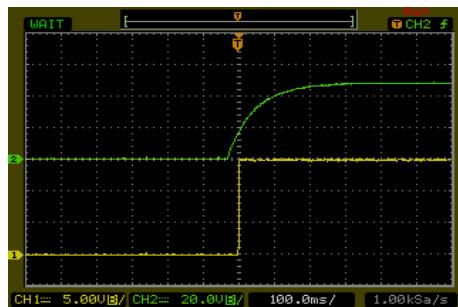
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



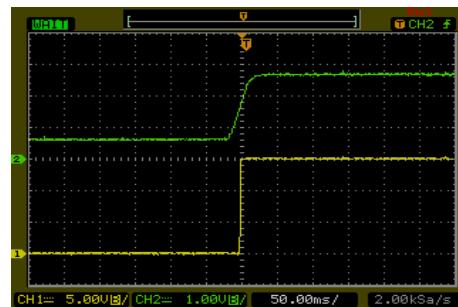
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



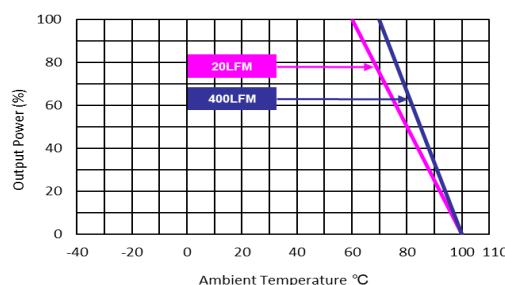
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

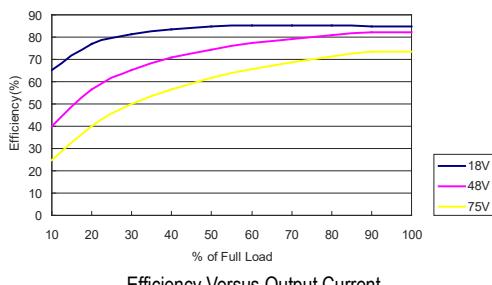


Derating Output Current Versus Ambient Temperature and Airflow

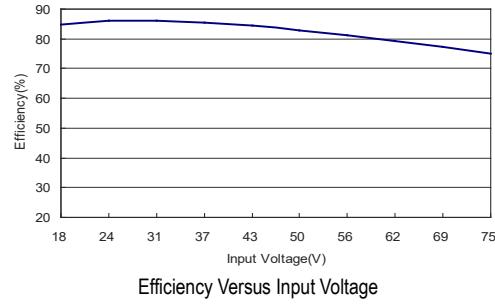
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-48S24

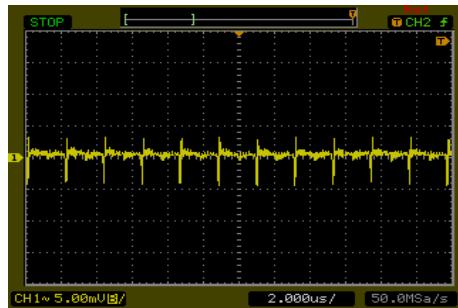


Efficiency Versus Output Current



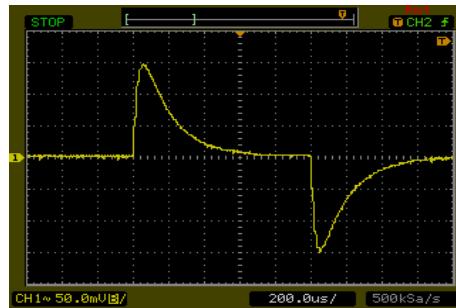
Efficiency Versus Input Voltage

Full Load



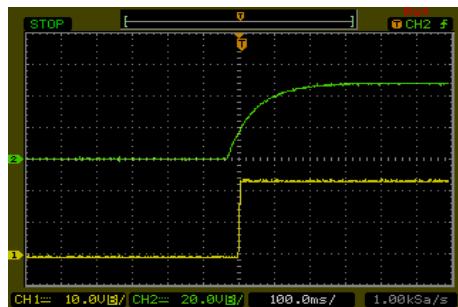
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



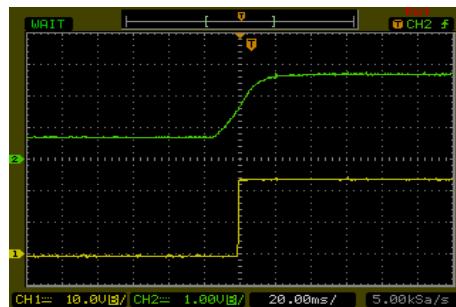
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



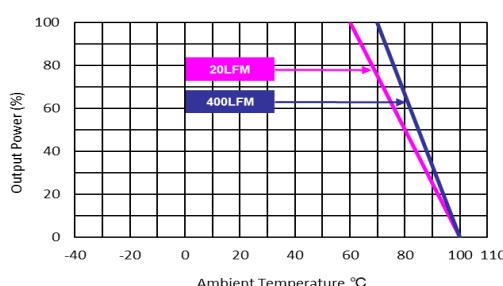
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

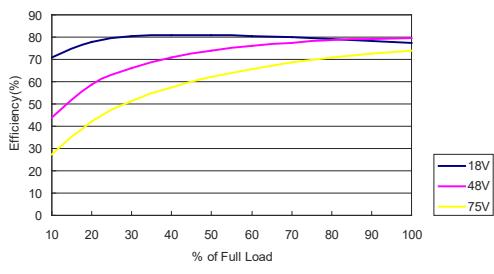


Derating Output Current Versus Ambient Temperature and Airflow

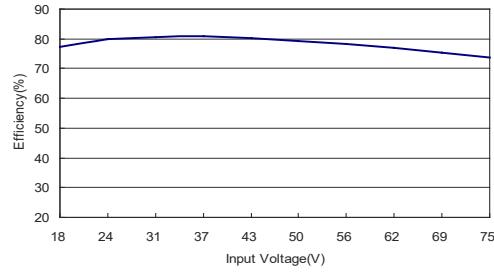
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-48D05

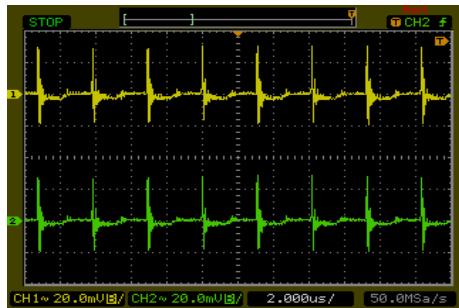


Efficiency Versus Output Current



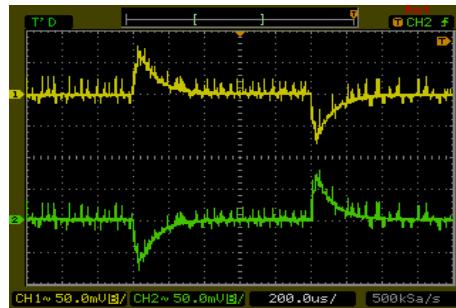
Efficiency Versus Input Voltage

Full Load



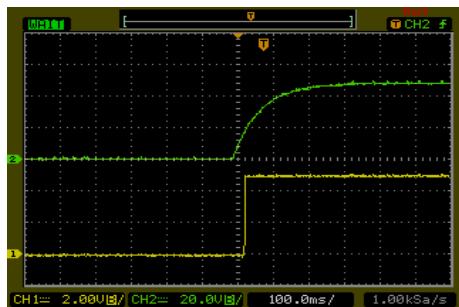
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



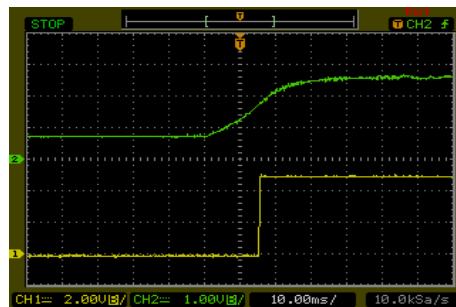
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



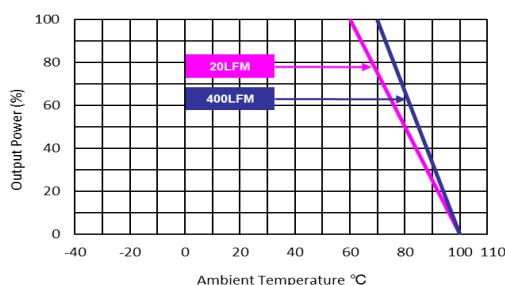
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

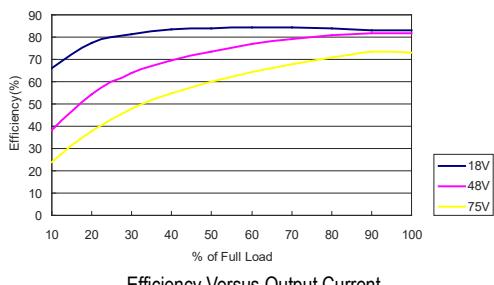


Derating Output Current Versus Ambient Temperature and Airflow

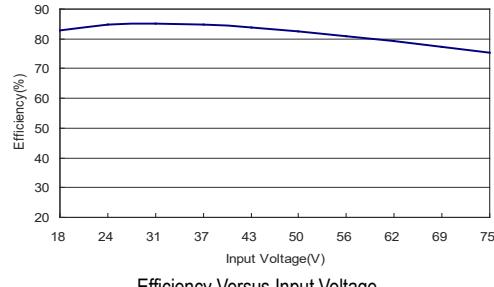
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-48D12

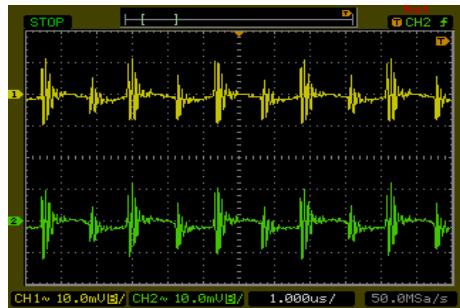


Efficiency Versus Output Current



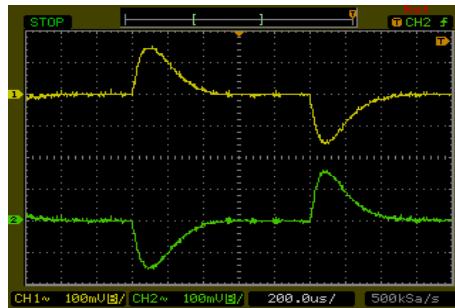
Efficiency Versus Input Voltage

Full Load



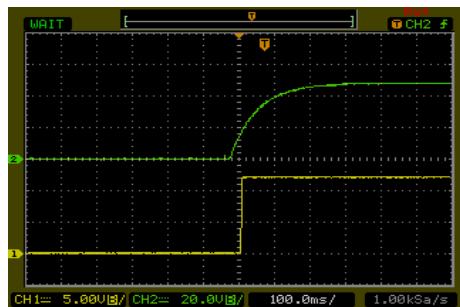
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



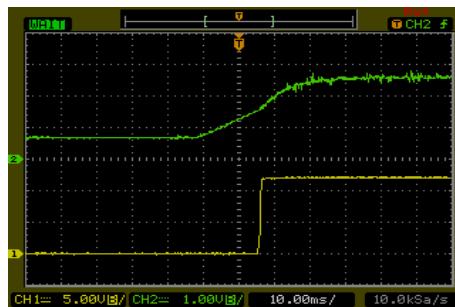
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



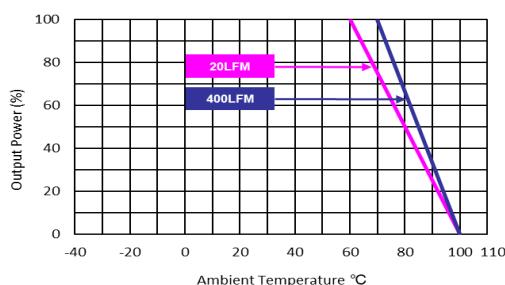
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load

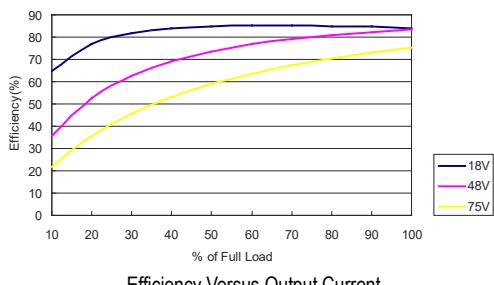


Derating Output Current Versus Ambient Temperature and Airflow

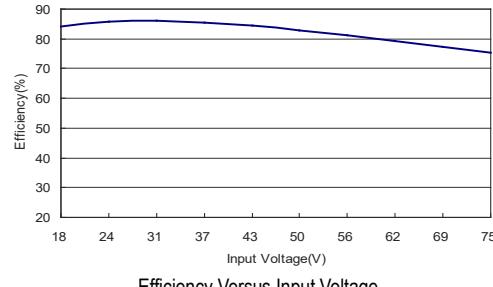
$V_{in}=V_{in\ nom}$

Characteristic Curves

All test conditions are at 25°C. The figures are identical for MSDWI03-48D15

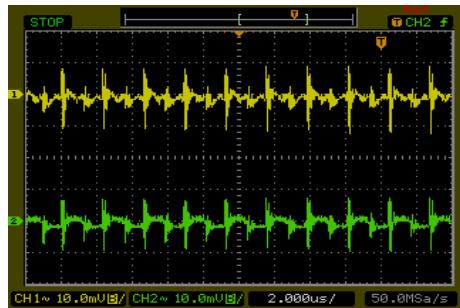


Efficiency Versus Output Current



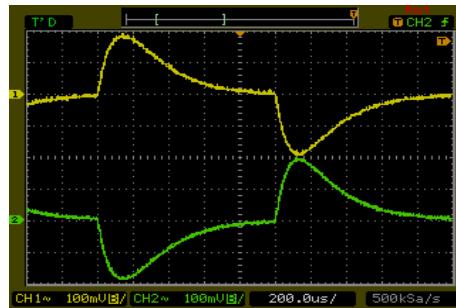
Efficiency Versus Input Voltage

Full Load



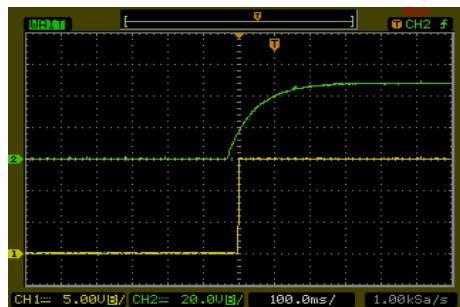
Typical Output Ripple and Noise

$V_{in}=V_{in\ nom}$; Full Load



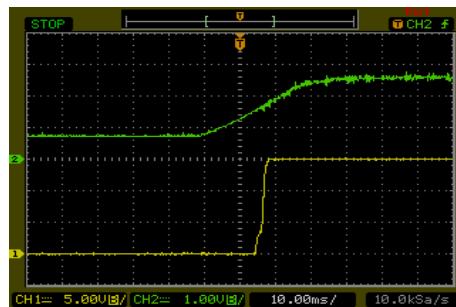
Transient Response to Dynamic Load Change

from 100% to 75% of Full Load; $V_{in}=V_{in\ nom}$



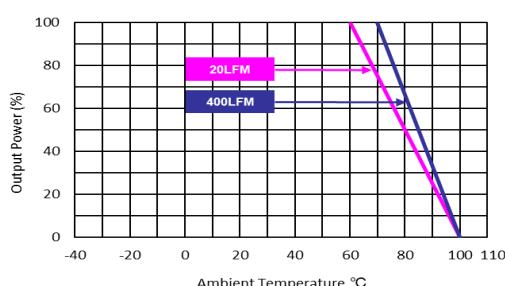
Typical Input Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



ON/OFF Voltage Start-Up and Output Rise Characteristic

$V_{in}=V_{in\ nom}$; Full Load



Derating Output Current Versus Ambient Temperature and Airflow

$V_{in}=V_{in\ nom}$

Package Specifications

Mechanical Dimensions		Connecting Pin Patterns	
<p>Top View dimensions: Total width 24.0 [0.94], Total height 18.1 [0.71], Pin pitch 1.00 [0.039], Pin 1 to 16 distance 3.11 [0.122], Pin 1 to 10 distance 15.24 [0.600], Pin 10 to 9 distance 2.54 [0.100]. Pin numbers 1, 2, 7, 8 are labeled.</p>		<p>Bottom View dimensions: Total width 12.4 [0.49], Total height 8.24 [0.324], Pin pitch 1.00 [0.039], Pin 1 to 16 distance 8.24 [0.324], Pin 1 to 10 distance 12.4 [0.49], Pin 10 to 9 distance 0.25 [0.010], Pin 1 to 2 distance 0.25 [0.010], Pin 1 to 16 distance 0.25 [0.010], Pin 1 to 8 distance 0.25 [0.010], Pin 1 to 7 distance 0.25 [0.010], Pin 1 to 9 distance 0.25 [0.010], Pin 1 to 10 distance 0.25 [0.010]. Pin numbers 1, 2, 7, 8, 10, 9, 16 are labeled. A seating plane angle of 0.5-4° is indicated.</p>	
<p>Pin Pattern diagram showing the layout of pins 1 through 16 on the package. Pin 1 is at the bottom left, followed by 2, 7, 8, 10, 9, 16, 1, 2, 7, 8, 10, 9, 16, 1, 2, 7, 8. Pin 1 is a +ve pin, and Pin 16 is a -ve pin. Pin 10 is a remote on/off pin.</p>		<p>Pin Pattern diagram showing the layout of pins 1 through 16 on the package. Pin 1 is at the bottom left, followed by 2, 7, 8, 10, 9, 16, 1, 2, 7, 8, 10, 9, 16, 1, 2, 7, 8. Pin 1 is a +ve pin, and Pin 16 is a -ve pin. Pin 10 is a remote on/off pin.</p>	

- ▶ All dimensions in mm (inches)
- ▶ Tolerance: X.X±0.25 (X.XX±0.01)
X.XX±0.13 (X.XXX±0.005)
- ▶ Pins ±0.05 (±0.002)

Pin Connections		
Pin	Single Output	Dual Output
1	-Vin	-Vin
2	Remote On/Off	Remote On/Off
7	NC	NC
8	NC	Common
9	+Vout	+Vout
10	-Vout	-Vout
16	+Vin	+Vin

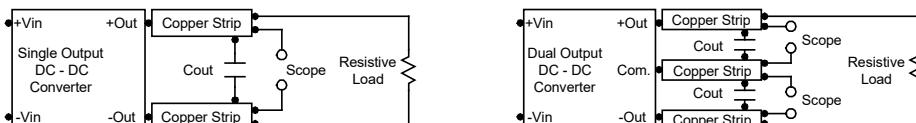
Physical Characteristics	
Case Size	: 24.0x13.7x8.0 mm (0.94x0.54x0.31 inches)
Case Material	: Plastic resin (flammability to UL 94V-0 rated)
Pin Material	: Phosphor Bronze
Weight	: 4.2g

NC: No Connection

Test Setup

Peak-to-Peak Output Noise Measurement Test

Use a C_{out} $0.47\mu F$ ceramic capacitor. Scope measurement should be made by using a BNC socket, measurement bandwidth is 0-20 MHz. Position the load between 50 mm and 75 mm from the DC-DC Converter.



Technical Notes

Remote On/Off

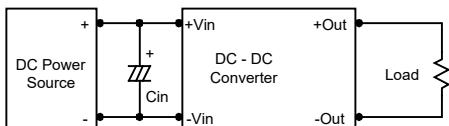
Positive logic remote on/off turns the module on during a logic high voltage on the remote on/off pin, and off during a logic low. To turn the power module on and off, the user must supply a switch to control the voltage between the on/off terminal and the -Vin terminal. The switch can be an open collector or equivalent. A logic low is -0.7V to 0.8V. A logic high is 2.5V to 5.5V. The maximum sink current of the switch at on/off terminal during a logic low is -300 μA . The maximum sink current of the switch at on/off terminal during a logic high is -200 μA or open.

Overcurrent Protection

To provide protection in a fault (output overload) condition, the unit is equipped with internal current limiting circuitry and can endure current limiting for an unlimited duration. At the point of current-limit inception, the unit shifts from voltage control to current control. The unit operates normally once the output current is brought back into its specified range.

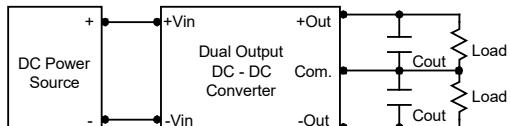
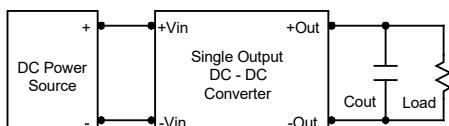
Input Source Impedance

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. In applications where power is supplied over long lines and output loading is high, it may be necessary to use a capacitor at the input to ensure startup. Capacitor mounted close to the power module helps ensure stability of the unit, it is recommended to use a good quality low Equivalent Series Resistance (ESR < 1.0 Ω at 100 kHz) capacitor of a $4.7\mu F$ for the 24V input devices and a $2.2\mu F$ for the 48V devices.



Output Ripple Reduction

A good quality low ESR capacitor placed as close as practicable across the load will give the best ripple and noise performance. To reduce output ripple, it is recommended to use $3.3\mu F$ capacitors at the output.

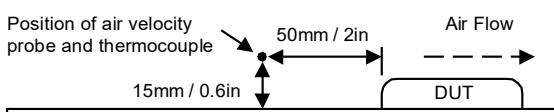


Maximum Capacitive Load

The MSDWI03 series has limitation of maximum connected capacitance at the output. The power module may be operated in current limiting mode during start-up, affecting the ramp-up and the startup time. The maximum capacitance can be found in the data sheet.

Thermal Considerations

Many conditions affect the thermal performance of the power module, such as orientation, airflow over the module and board spacing. To avoid exceeding the maximum temperature rating of the components inside the power module, the case temperature must be kept below 105°C. The derating curves are determined from measurements obtained in a test setup.



Remote On/Off Implementation

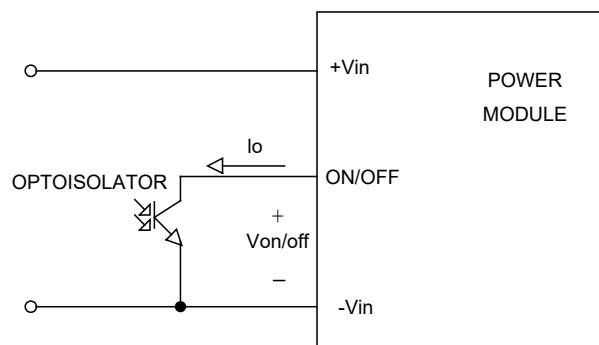
The positive logic remote ON/OFF control circuit is included.

Turns the module ON during logic High on the ON/OFF pin and turns OFF during logic Low. The ON/OFF input signal ($V_{on/off}$) that referenced to GND. If not using the remote on/off feature, please open circuit between on/off pin and $-V_{in}$ pin to turn the module on.

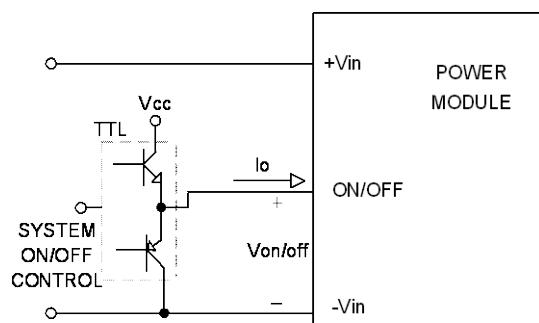
The negative logic remote ON/OFF control circuit is included.

Turns the module ON during logic Low on the On/Off pin and turns OFF during logic High. The On/Off pin is an open collector/drain logic input signal ($V_{on/off}$) that referenced to GND. If not using the remote on/off feature. Please short circuit between on/off pin and $-V_{in}$ pin to turn the module on.

Remote ON/OFF implementation



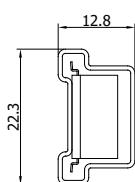
Isolated-Closure Remote ON/OFF



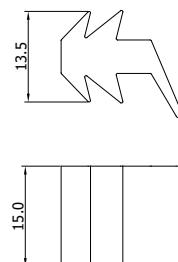
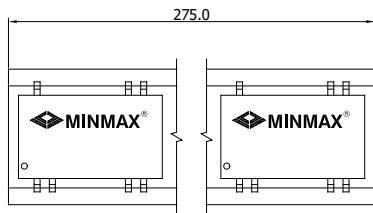
Level Control Using TTL Output

Packaging Information for Tube

Tube



Plug

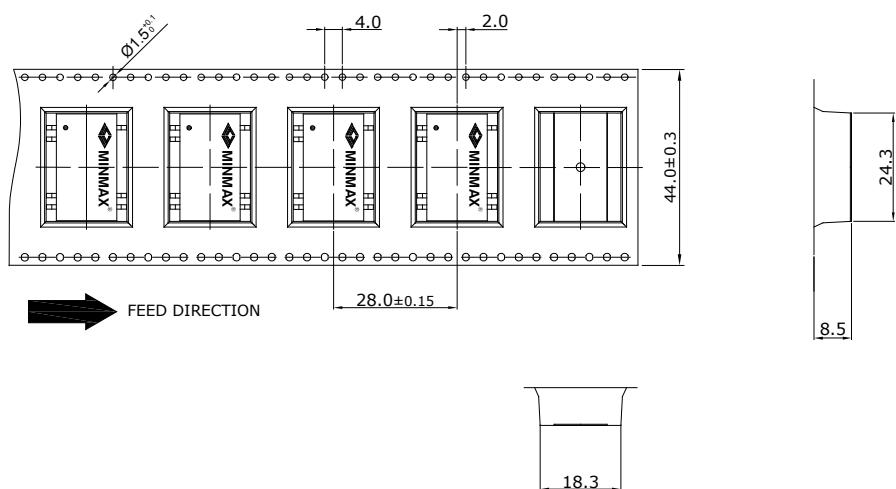


Unit: mm

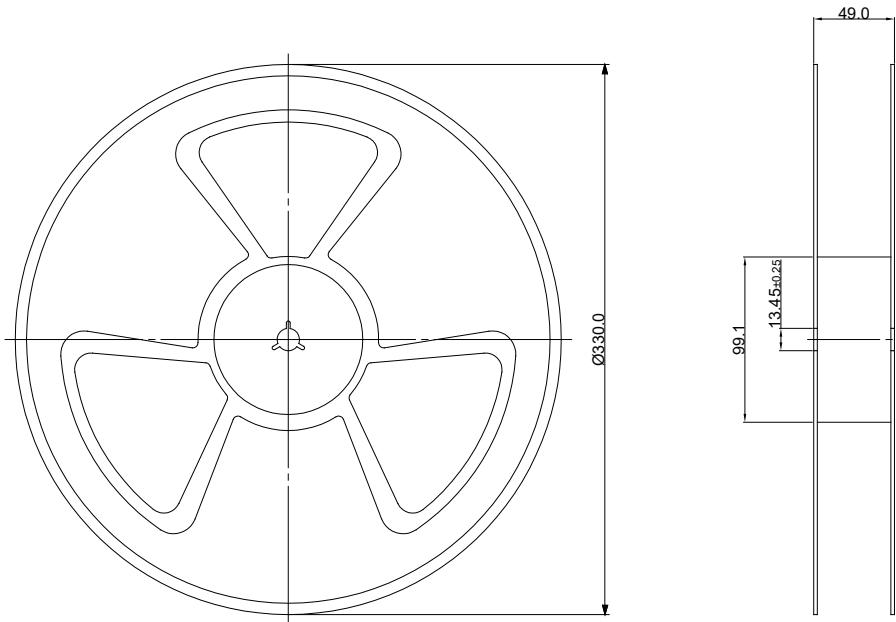
10 PCS per TUBE

Packaging Information for Tape & Reel

Tape



Reel



Packaging Style	Quantity
With Heatsink Tube	N/A
Tape and Reel to IEC 286-3 Specifications	250

Soldering and Reflow Considerations

Profile	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate(T_s max. To T_p)	3°C/second max.	3°C/second max.
Preheat		
· Temperature Min ($T_{s\min}$)	100°C	150°C
· Temperature Max ($T_{s\max}$)	150°C	200°C
· Time ($T_{s\min}$ to $T_{s\max}$) (ts)	60~120 seconds	60~180 seconds
Time maintained above:		
· Temperature (T_L)	183°C	217°C
· Time (t_L)	60~150 seconds	60~150 seconds
Peak Temperature (T_p)	See Table 4-1	See Table 4-2
Time within 5°C of actual Peak	10~30 seconds	20~40 seconds
Temperature (t_p) ²		
Ramp-down Rate	6°C/second max.	6°C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

Note 1: All temperatures refer to topside of the package, measured on the package body surface.

Note 2: Time within 5°C of actual peak temperature (t_p) specified for the reflow profiles is a "supplier" minimum and "user" maximum.

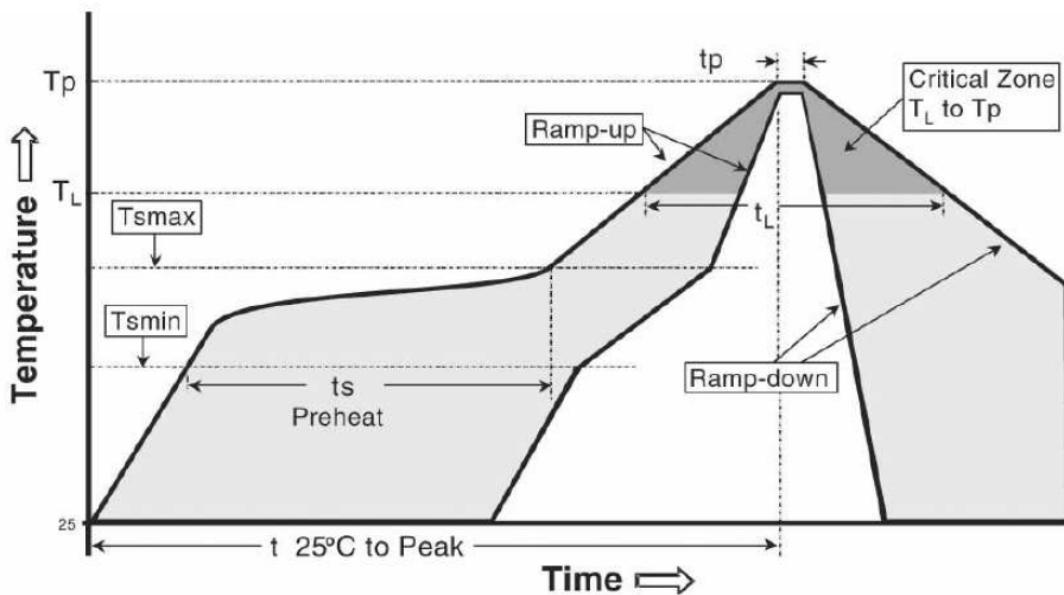


Table 4-1 SnPb Eutectic Process-Classification Temperatures (T_c)

Package Thickness	Volume mm ³	Volume mm ³
<350	<350	≥ 350
<2.5mm	235°C	220°C
$\geq 2.5\text{mm}$	220°C	220°C

Table 4-2 Pb-Free Process-Classification Temperatures (T_c)

Package Thickness	Volume mm ³	Volume mm ³	Volume mm ³
<350	<350	350-2000	>2000
<1.6mm	260°C	260°C	260°C
1.6mm-2.5mm	260°C	250°C	245°C
$\geq 2.5\text{mm}$	250°C	245°C	245°C

Part Number Structure

M	SD	WI	03	-	05	S	05
	Package Type SMD-16	Ultra-wide 4:1 Input Voltage Range	Output Power 3 Watt		Input Voltage Range 24: 9 ~ 36 VDC 48: 18 ~ 75 VDC	Output Quantity S: Single D: Dual	Output Voltage 033: 3.3 VDC 05: 5 VDC 12: 12 VDC 15: 15 VDC 24: 24 VDC

MTBF and Reliability

The MTBF of MSDWI03 series of DC-DC converters has been calculated using

MIL-HDBK 217F NOTICE2, Operating Temperature 25°C, Ground Benign.

Model	MTBF	Unit
MSDWI03-24S033	839,842	
MSDWI03-24S05	837,942	
MSDWI03-24S12	846,238	
MSDWI03-24S15	854,847	
MSDWI03-24S24	860,437	
MSDWI03-24D05	836,750	
MSDWI03-24D12	850,051	
MSDWI03-24D15	850,051	
MSDWI03-48S033	839,842	
MSDWI03-48S05	841,468	
MSDWI03-48S12	846,668	
MSDWI03-48S15	852,660	
MSDWI03-48S24	858,516	
MSDWI03-48D05	841,538	
MSDWI03-48D12	851,499	
MSDWI03-48D15	848,320	

Hours