FEATURES

- ► Industrial SMD Package
- ► I/O Isolation 1000 VDC
- ▶ Operating Ambient Temp. Range -40°C to +85°C
- ► Water-washable Process Available
- Qualified for Lead-free Reflow Solder Process According to IPC/JEDEC J-STD-020D.1
- ► Tape & Reel Package Available









PRODUCT OVERVIEW

The MINMAX MSAU400 series is a range of 2W DC-DC converters in a SMD- Package featuring I/O-isolation of 1000VDC. The small footprint makes this product the ideal solution for many applications where a voltage has to be isolated i.e for noise reduction, ground loop elimination, in digital interfaces or where a converted voltage is required.

A high efficiency allows an operating temperature range of–40°C to +85°C(with derating). These converters are fully qualified for the higher temperature profile used in lead-free reflow solder processes. For automated SMD production lines the product can also be supplied in tape& reel package.

Model Selec	ction Guide								
Model Number	Input Voltage	Output Voltage	Output (Current	Input C	Current	Load Regulation	Max. capacitive Load	Efficiency (typ.)
	(Range)		Max.	Min.	@Max. Load	@No Load			@Max. Load
	VDC	VDC	mA	mA	mA(typ.)	mA(typ.)	% (max.)	μF	%
MSAU401		5	400	8	519		11	47	77
MSAU403	_ [12	165	3	488		11	10	81
MSAU406	5 (4.5, 5.5)	±5	±200	±4	519	60	12	10#	77
MSAU408	(4.5 ~ 5.5)	±12	±83	±1.5	504		7	4.7#	79
MSAU409		±15	±66	±1	501		7	4.7#	79
MSAU411		5	400	8	213		8	47	78
MSAU413	12	12	165	3	201	20	6	10	82
MSAU418	(10.8 ~ 13.2)	±12	±83	±1.5	202	30	5	4.7#	82
MSAU419		±15	±66	±1	201		5	4.7#	82
MSAU421		5	400	8	106		8	47	78
MSAU423	24	12	165	3	101	45	5	10	81
MSAU428	(21.6 ~ 26.4)	±12	±83	±1.5	102	15	5	4.7#	81
MSAU429		±15	±66	±1	100		5	4.7#	82

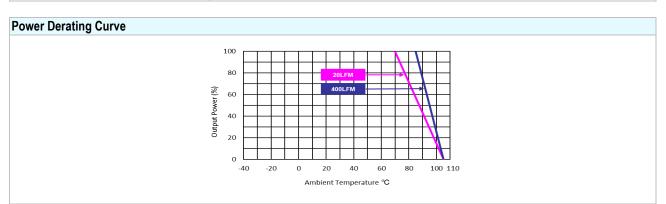
For each output

Input Specifications					
Parameter	Model	Min.	Тур.	Max.	Unit
	5V Input Models	4.5	5	5.5	
Input Voltage Range	12V Input Models	10.8	12	13.2	
	24V Input Models	21.6	24	26.4	VDC
	5V Input Models	-0.7		9	VDC
Input Surge Voltage (1 sec. max.)	12V Input Models	-0.7		18	
	24V Input Models	-0.7		30	
Input Filter	All Models		Internal (Capacitor	

Output Specifications						
Parameter	Conditions	Min.	Тур.	Max.	Unit	
Output Voltage Setting Accuracy			±1.5	±4.0	%Vnom.	
Output Voltage Balance	Dual Output, Balanced Loads		±0.1	±1.0	%	
Line Regulation	For Vin Change of 1%		±1.2	±1.5	%	
Load Regulation	Io=20% to 100%		See Model S	el Selection Guide		
Ripple & Noise	0-20 MHz Bandwidth			120	mV _{P-P}	
Temperature Coefficient			±0.01	±0.02	%/°C	
Short Circuit Protection	0.5 Second	Max., Automatic	Recovery			

General Specifications					
Parameter	Conditions	Min.	Тур.	Max.	Unit
I/O la alatia a Valta da	60 Seconds	1000			VDC
I/O Isolation Voltage	1 Seconds	1200			VDC
I/O Isolation Resistance	500 VDC	1000			MΩ
I/O Isolation Capacitance	100kHz, 1V		60	100	pF
Switching Frequency		50	100	120	kHz
MTBF (calculated)	MIL-HDBK-217F@25°C, Ground Benign		2,000,000		Hours
Moisture Sensitivity Level (MSL)	IPC/JEDEC J-STD-020D.1		Lev	el 3	

Environmental Specifications				
Parameter	Conditions	Min.	Max.	Unit
Operating Ambient Temperature Range (See Power Derating Curve)		-40	+85	°C
Case Temperature			+105	°C
Storage Temperature Range		-50	+125	°C
Humidity (non condensing)			95	% rel. H
Lead-free Reflow Solder Process	IPC/JEE	DEC J-STD-020D.1		

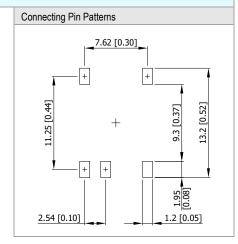


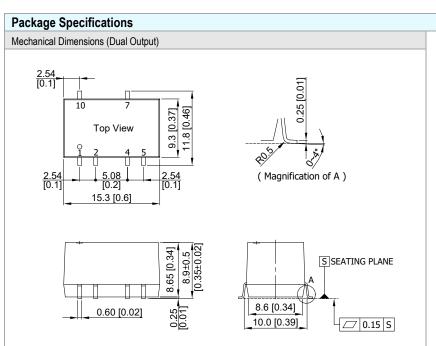
Notes

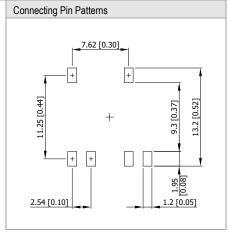
- 1 Specifications typical at Ta=+25°C, resistive load, nominal input voltage and rated output current unless otherwise noted.
- 2 These power converters require a minimum output loading to maintain specified regulation, operation under no-load conditions will not damage these modules; however they may not meet all specifications listed.
- We recommend to protect the converter by a slow blow fuse in the input supply line.
- 4 Other input and output voltage may be available, please contact MINMAX.
- 5 Specifications are subject to change without notice.
- The repeated high voltage isolation testing of the converter can degrade isolation capability, to a lesser or greater degree depending on materials, construction, environment and and reflow solder process. Any material is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage. Furthermore, the high voltage isolation capability after reflow solder process should be evaluated as it is applied on system.

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Package Specifications Mechanical Dimensions (Single Ouput) 2.57 [0.1] 11.8 [0.46] 9.3 [0.37] Top View (Magnification of A) 12.7 [0.5] 8.9±0.5 [0.35±0.02] 8.65 [0.34] SEATING Ş 8.6 [0.34] 0.60 [0.02] 10.0 [0.39]







- ► All dimensions in mm (inches)
- ➤ Tolerance: X.X±0.25 (X.XX±0.01) X.XX±0.13 (X.XXX±0.005)
- ➤ Pins ±0.05(±0.002)

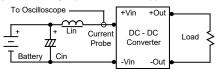
Pin Connect	tions	
Pin	Single Output	Dual Output
1	-Vin	-Vin
2	+Vin	+Vin
4	-Vout	Common
5	+Vout	-Vout
7	No Pin	+Vout
8	NA	No Pin
10	No Pin	NA

Case Size (Single Output)	:	12.7x9.3x8.65mm (0.50x0.37x0.34 inches)
Case Size (Dual Output)	:	15.3x9.3x8.65mm (0.60x0.37x0.34 inches)
Case Material	:	Molding (flammability to UL 94V-0 rated)
Weight (Single Output)	:	1.5g
Weight (Dual Output)		2.2g

Test Setup

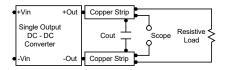
Input Reflected-Ripple Current Test Setup

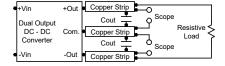
Input reflected-ripple current is measured with a inductor Lin $(4.7\mu\text{H})$ and Cin $(220\mu\text{F}, \text{ESR} < 1.0\Omega \text{ at } 100 \text{ kHz})$ to simulate source impedance. Capacitor Cin, offsets possible battery impedance. Current ripple is measured at the input terminals of the module, measurement bandwidth is 0-500 kHz.



Peak-to-Peak Output Noise Measurement Test

Use a Cout 0.33µF ceramic capacitor. Scope measurement should be made by using a BNC socket, measurement bandwidth is 0-20 MHz. Position the load between 50 mm and 75 mm from the DC-DC Converter.





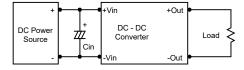
Technical Notes

Maximum Capacitive Load

The MSAU400 series has limitation of maximum connected capacitance at the output. The power module may be operated in current limiting mode during start-up affecting the ramp-up and the startup time. The maximum capacitance can be found in the data sheet.

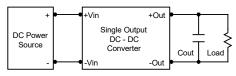
Input Source Impedance

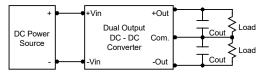
The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. In applications where power is supplied over long lines and output loading is high, it may be necessary to use a capacitor at the input to ensure startup. Capacitor mounted close to the power module helps ensure stability of the unit, it is comended to use a good quality low Equivalent Series Resistance (ESR < 1.0Ω at 100 kHz) capacitor of a $2.2\mu\text{F}$ for the 5V input devices, a $1.0\mu\text{F}$ for the 12V input devices and a $0.47\mu\text{F}$ for the 24V devices.



Output Ripple Reduction

A good quality low ESR capacitor placed as close as practicable across the load will give the best ripple and noise performance. To reduce output ripple, it is recommended to use 1.5µF capacitors at the output.





Thermal Considerations

Many conditions affect the thermal performance of the power module, such as orientation, airflow over the module and board spacing. To avoid exceeding the maximum temperature rating of the components inside the power module, the case temperature must be kept below 105°C. The derating curves are determined from measurements obtained in a test setup.

